

## COMPANY PROFILE

- **Experts in Digital IP design, SoC creation & EDA tool development**
  - Architecture, RTL Design, Verification, Synthesis, EDA tools, CAD flows, ...
  - Expertise & processes in place for high-quality & on-time delivery
  - Launched design IP products in Feb 2015, EDA products to follow
- **Proven track record in delivering differentiated solutions**
  - Successful customer projects in  $\mu$ Arch & design, RTL IP dev, EDA tool dev
  - Founders possess long-term top 5 semiconductor company pedigree
  - Designed & implemented several market leading products over many years
- **Started operations in Jan 2013 with HQ in Bangalore**
  - Infrastructure and tools in place for digital system design & verification
  - Leadership team possesses 50+ years of experience in VLSI domain
  - Stable design team, handpicked from top colleges in India (IITs/NITs)

## DOMAIN EXPERTISE

- **Memory**
  - DDR4/3/2, LPDDR 3/2, ...
  - Internal Memory Controllers
- **Communications**
  - Turbo/Viterbi decoder
  - Ethernet MAC, TCP/UDP offload
- **Video (H.264/AVC, H.265/HEVC)**
  - Motion estimation & compensation, Intra prediction, Transforms (DCT/Hadamard), Deblocking filter, Entropy coding (CABAC/CAVLC), ...
- **Security**
  - AES/Kasumi/Snow3G encryption/decryption
- **SoC Realization**
  - Bus protocols (AXI/OCP), USB, PCI/PCIe, ...
  - Complex many-core (DSP+ARM) systems, shared memory architectures, ...
- **EDA Tool Development**
  - Embedded memory compiler, RTL compilation, HDL Simulation, ...
  - H.264 bit stream generation
  - Design methodology & CAD flows

## FOUNDING TEAM

**Bhaskar Karmakar** has 20 years of experience in software design & development, engineering management and customer interactions in semiconductor and EDA companies. He has successfully managed high-performance engineering teams across multiple technology domains for many years. Bhaskar holds a Bachelors' Degree in Electrical Engineering from Indian Institute of Technology (IIT), Kharagpur. He is a Senior Member of the IEEE, and has held the title of MGTS on the Texas Instruments Technical Ladder.

**Sajish Sajayan** has worked for 18 years in the semiconductor industry. His expertise spans the entire front-end design domain in SoC and IP, including SoC architecture, micro-architecture, RTL implementation, design verification, logic synthesis, timing analysis and timing closure. Sajish holds a Bachelors' Degree in Electronics & Communication Engineering from the National Institute of Technology (NIT), Calicut. He has 5 granted US patents to his name, with another 3 pending in USPTO, and was elected MGTS on the TI Technical Ladder.

**Sivakumar Ramaiyan** has 20 years of experience in the semiconductor industry with a track record of delivering exceptional quality on-time. He is a recognized expert in multiple aspects of semiconductor design having worked on specification creation, RTL design, micro-architecture, synthesis, static timing analysis and equivalence checking for IP cores in multimedia SoCs. Sivakumar holds a Bachelors' Degree in Electrical & Electronics Engineering from PSG College of Technology, India. He has been granted 2 patents in semiconductor design by the USPTO.